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GENERAL DESCRIPTION

The EM84530 Mouse Controller is specially designed to control serial mouse or PS/2 mouse device. This single chip can interface three key-switches and 4 X-Y photo-couples directly to RS-232C or 8042 controller.
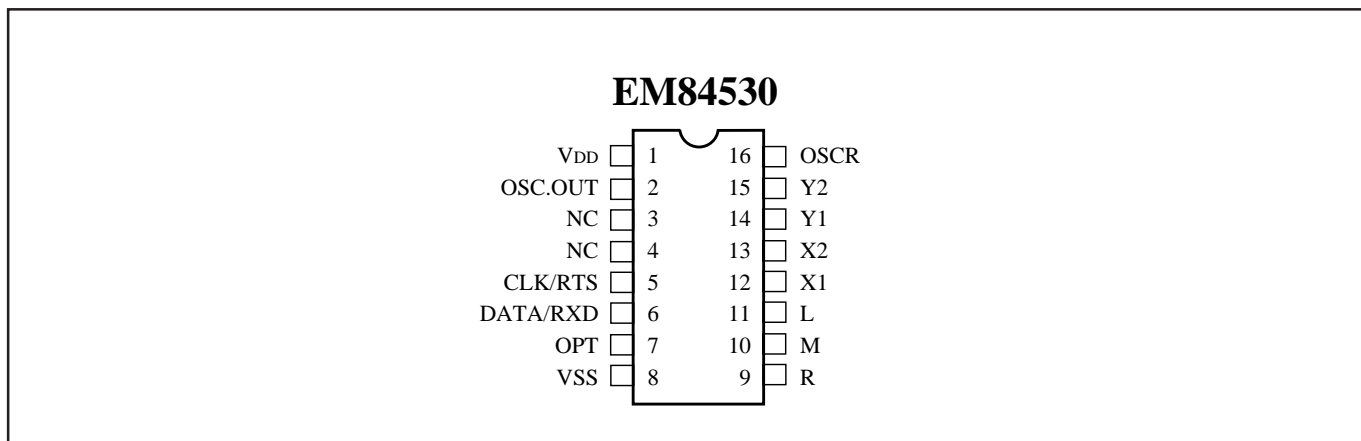
FEATURES

- Include PnP software scrolling function
- Using 50kΩ ±1% resistor for RC oscillation.
- Compatible with legacy PS/2 mouse.
- Compatible with Microsoft serial mouse.
- Compatible with Microsoft PS/2 mouse.
- Serial or PS/2 port auto-detect.
- 100 bytes PnP ID code could be filled by code layer.
- Built-in noise immunity circuit.
- The sampiling rate of motion detector is up to 65KHz.
- Built-in three zener diodes : one in VDD to VSS, two in RTS to VDD.
- CMOS push-pull RXD output.
- Built-in current comparator for photo-couples input.
- Built-in three step dynamic input impedance.
- Three key-switches and four photo-couples inputs.
- RTS debouncing circuit included.
- Photo couple test mode included.
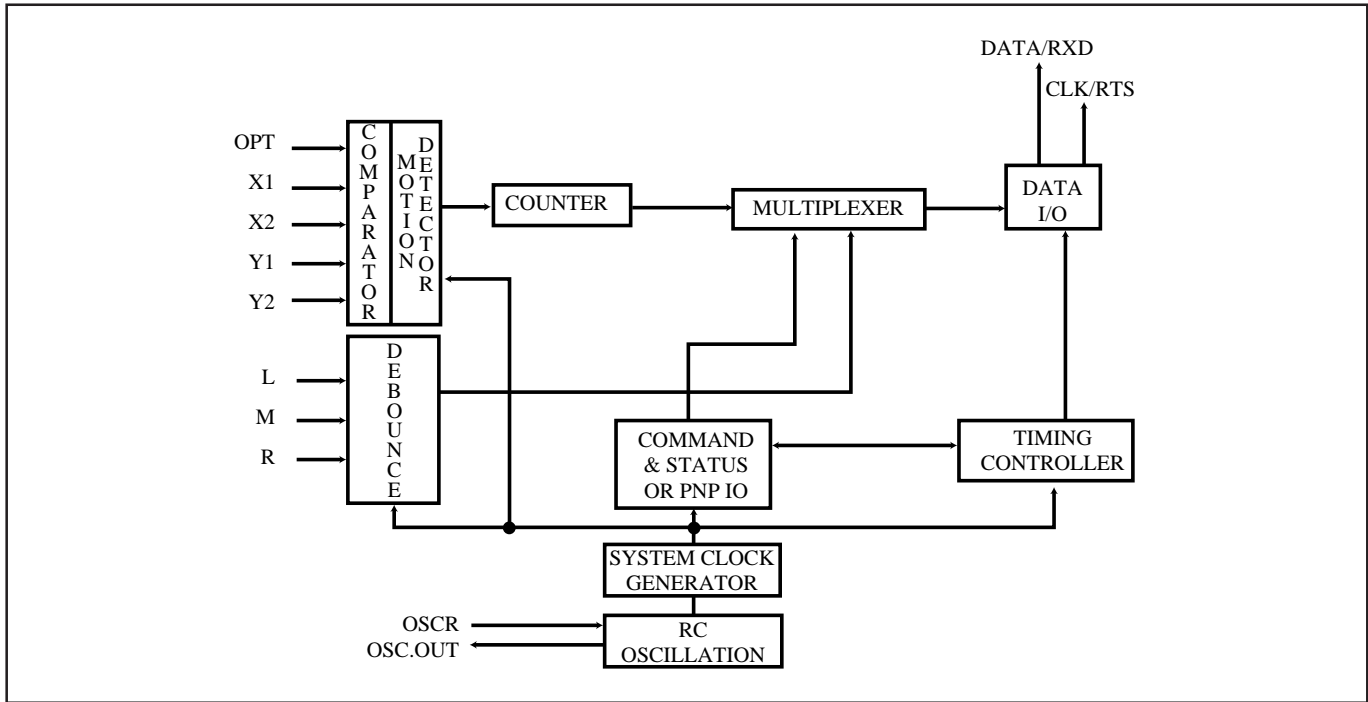- Low power dissipation.

APPLICATION

- Serial PnP mouse.
- PS/2 mouse.
- Combo mouse.

PIN ASSIGNMENT

EM84530

VDD	1	16 OSCR
OSC.OUT	2	15 Y2
NC	3	14 Y1
NC	4	13 X2
CLK/RTS	5	12 X1
DATA/RXD	6	11 L
OPT	7	10 M
VSS	8	9 R

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FUNCTIONAL BLOCK DIAGRAM

PIN DESCRIPTIONS

Symbol	I/O	Function
OPT	I/O	INPUT: 200kΩ pull low to VSS. When OPT is connected to VDD, EM84530 will enter test mode. In test mode, L will be the output of X1,while M will be the output of X2. Toggling R key can change these output to be Y1,Y2 respectively.
OSC.OUT	O	The RC oscillation clock output. Or 3mA sink current output for X, Y photo emitters.
OSCR	I	Connect 50kΩ±1% precise resistor for oscillation.
CLK/RTS	I/O	Connect directly to 8042 auxiliary port CLK line in PS/2 mode or connect directly to RS-232C RTS pin in serial mode. Auto-detect the operating port by this pin.
DATA/RXD	I/O	Connect directly to 8042 auxiliary port DATA line in PS/2 mode or connect directly to RS-232C RXD pin in serial mode.
R M L	I I/O I/O	Three key-switches inputs. 200kΩ resistor pull low.
X1 X2 Y1 Y2	I I I I	Three step dynamic input impedance. If OSC.OUT is not connected to LEDs, the dynamic input impedance will be off. Use current comparator to measure photo-couples "ON", or "OFF".
VSS		Negative power.
VDD		Power line.

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FUNCTION DESCRIPTIONS

(I) Serial Mouse :

In Microsoft mode, the transmission cycle consists of four bytes in one report. Each byte contains one start bit, 7 data bits and two stop bits. The first byte contains "L", "R" key status and four bits of the two most significant bits in horizontal counter and vertical counter. The second byte represents the value accumulated by horizontal counter, the third byte is the data of the vertical counter. The last byte contains only "M" key status. The vertical data is transmitted in 2's complement.

Output byte arrange

Bit no.	......	6	5	4	3	2	1	0
1st byte	......	1	L	R	V7'	V6'	H7	H6
2nd byte	....	0	H5	H4	H3	H2	H1	H0
3rd byte	....	0	V5'	V4'	V3'	V2'	V1'	V0'
4th byte	....	0	0	M	0	0	0	0

Plug & Play ID code

When RTS input from low to high, EM83810 will send a string ID code. Customer can specify the contains of the code area and the total lenth below 100 bytes by changing the code layer.

Field Name	Length	Field Data	Description
Other ID	<17	4D, 5A, 40, 00, 00, 00	ID for legacy
Begin ID	1	08	Begin PnP ID
PnP Revision	2	01, 24	Version 1.00
EISA ID	3	25, 2D, 23	EMC
Product ID	4	10, 10, 10, 11	0001
Serial Number	9	3C	None provided
Class ID	<34	3C, 2D, 2F, 35, 33, 25	Mouse Class
Driver ID	<42	3C, 30, 2E, 30, 10 26, 10, 21	PnP0F0A
User Name	<42	3C, 25, 2D, 23, 00, 33, 23, 32, 2F, 2C, 2C, 29, 2E, 27, 00, 33, 25, 32, 29, 21, 2C, 00, 2D, 2F, 35, 33, 25	EMC SCROLLING SERIAL MOUSE
Checks um	2	21, 15	2 bytes checks um
End PnP	1	09	End PnP ID

(II) Legacy PS/2 mouse operating:

 (A) Operating mode
   There are four operating modes in PS/2 mouse:
   a. Reset Mode:
     In this mode a self-test is initiated during power-on or by a Reset command. After reset signal, PS/2 mouse will
     (a) Send completion code AA & ID code 00.
     (b) Set default :

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sampling rate: 100 reports/s
non-autospeed
stream mode
2 dot/count
disable

b. Stream Mode:
   The maximum rate of transfer is the programmed sample rate.
   Data report is transmitted if
   (a) switch is pressed
   (b) movement has been detect

c. Remote Mode:
   Data is transmitted only in response to a Read Data command.

d. Wrap Mode:
   Any byte of data sent by the system,   except hex EC (Reset wrap mode) or hex FF (Reset), is returned
   by EM84530.

B). PS/2 Mouse Data Report:

a).   In stream mode: A data report is sent at the end of a sample interval.

b).   In remote mode: A data report is sent in response to Read Data command.

c).   Data report format:

Byte	Bit	Description
1	0	Left button status; 1 = pressed
	1	Right button status; 1 = pressed
	2	Middle button status; 1 = pressed
	3	Reserve
	4	X data sign; 1 = negative
	5	Y data sign; 1 = negative
	6	X data overflow; 1 = overflow
	7	Y data overflow; 1 = overflow
2	0-7	X data (D0 - D7)
3	0-7	Y data (D0 - D7)

C) PS/2 mouse Data Transmission:

a). EM84530 generates the clocking signal when sending data to and receiving data from the system.

b). The system requests EM84530 receive system data output by forcing the DATA line to an inactive level
     and allowing CLK line to go to an active level.

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c). Data transmission frame:

Bit	Function
1	Start bit (always 0)
2-9	Data bits (D0 - D7)
10	Parity bit (odd parity)
11	Stop bit (always 1)

d). Data Output (data from EM84530 to system):

If CLK is low (inhibit status) , data is no transmission.
If CLK is high and DATA is low (request-to-send), data is updated. Data is received from the system and no transmission are started by EM84530 until CLK and DATA both high. If CLK and DATA both are high, the transmission is ready. DATA is valid prior to the falling edge of CLK and beyond the rising edge of CLK. During transmission, EM84530 check for line contention by checking for an inactive level on CLK at intervals not to exceed 100u sec. Contention occurs when the system lowers CLK to inhibit EM84530 output after EM84530 has started a transmission. If this occurs before the rising edge of the tenth clock, EM84530 internal store its data in its buffer and returns DATA and CLK to an active level. If the contention does not occur by the tenth clock, the transmission is complete.
Following a transmission, the system inhibits EM84530 by holding CLK low until it can service the input or until the system receives a request to send a response from EM84530.

e). Data Input (from system to EM84530):

The system first check if EM84530 is transmitting data. If EM84530 is transmitting, the system can override the output forcing CLK to an inactive level prior to the tenth clock. If EM84530 transmission is beyond the tenth clock, the system receives the data. If EM84530 is not transmitting or if the system choose to override the output, the system force CLK to an inactive level for a period of not less than 100μ sec while preparing for output. When the system is ready to output start bit (0), it allows CLK go to active level. If request-to-send is detected, EM84530 clocks 11 bits. Following the tenth clock EM84530 checks for an active level on the DATA line, and if found, force DATA low , and clock once more. If occurs framing error, EM84530 continue to clock until DATA is high, then clocks the line control bit and request a Resend. When the system sends out a command or data transmission that requires a response, the system waits for EM84530 to response before sending its next output.

D). PS/2 Mouse Error Handling:

a). A Resend command (FE) following receipt of an invalid input or any input with incorrect parity.

b). If two invalid input are received in succession, an error code of hex (FC) is send to the system.

c). The counter accumulators are cleared after receiving any command except "Resend".

d). EM84530 receives a Resend command (FE), it transmit its last packet of data.

e). In the stream mode "Resend" is received by EM84530 following a 3-byte data packet transmission to the system. EM84530 resend the 3-byte data packet prior to clearing the counter.

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f).   A response is sent within 25 ms if

   a). The system requires a response

   b). An error is detected in the transmission

g).   If EM84530 is in the stream mode :

   a). The system disables EM84530 before issuing any command requiring a response.

   b). When a command requiring a response is issued by the system ,another command should not be issue until either the response is received or 25ms has passed.

E). PS/2 Mouse Commands Description:

There are 16 valid commands that transmits between the system and EM84530. The "FA" code is always the first response to any valid input received from the system other than a Set Wrap Mode or Resend command. The following table list the commands:

Hex Code	Command	Echo code
FF	Reset	FA,AA,00
FE	Resend	XX,(XX,XX)
F6	Set Default	FA
F5	Disable	FA
F4	Enable	FA
F3,XX	Set Sampling Rate	FA,FA
F2	Read Device Type	FA,00
F0	Set Remote Mode	FA
EE	Set Wrap Mode	FA
EC	Reset Wrap Mode	FA
EB	Read Data	FA,XX,XX,XX
EA	Set Stream Mode	FA
E9	Status Request	FA,XX,XX,XX
E8,XX	Set Resolution	FA,FA
E7	Set Auto-speed	FA
E6	Reset Auto-speed	FA

The following describes valid commands:

a). Reset (FF)
   EM84530 operation:
   a). Completion the reset.
   b). Transmitted FA, AA, 00 to the system.
   c). Set default:
      sampling rate: 100 reports/s
      non-autospeed
      stream mode
      2 dots/count
      disable

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b). Resend (FE)

    a). Any time EM84530 receives an invalid command, it returns a Resend command to the system.

    b). When EM84530 receives a Resend command, it retransmits its last packet of data. If the last packet was a Resend command, it transmits the packet just prior to the Resend command.

    c). In stream mode, if a Resend command is received by EM84530 immediately following a 3-byte data packet transmission to the system.

c). Set Default (F6)

    The command reinitializes all conditions to the power-on defaults.

d). Disable (F5)

    This command is used in the stream mode to stop transmissions from EM84530.

e). Enable (F4)

    Begins transmissions, if in stream mode.

f). Set Sampling Rate (F3,XX)

    In the stream mode, this command sets the sampling rate to the value indicated by byte hex XX, shown in following:

Second byte XX	Sample Rate
0A	10/sec
14	20/sec
28	40/sec
3C	60/sec
50	80/sec
64	100/sec
C8	200/sec

g). Read Device Type (F2)

    EM84530 always echoes " FA,00 " following this command.

h). Set Remote Mode (F0)

    Data value are reported only in response to a Read Data command.

i). Set Wrap Mode (EE)

    Wrap mode remains until Reset (FF) or Reset Wrap Mode (EC) is received.

j). Reset Wrap Mode (EC)

    EM84530 returns to the previous mode of operation after receiving this command.

k). Read Data (EB)

    This command is executed in either remote or stream mode. The data is transmitted even if there has been no movement since the last report or the button status is unchanged. Following a Read Data command, the registers are cleared after a data transmission.

l). Set Stream Mode (EA)

    This command sets EM84530 in stream mode.

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* This specification are subject to be changed without notice.

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m). Status Request (E9)

When this command is issued by the system, EM84530 respond with a 3-byte status report as follows:

Byte	Bit	Description
1	0	1 = Right button pressed
	1	1 = Middle button pressed
	2	1 = Left button pressed
	3	Reserved
	4	0 = Normal speed, 1 = Autospeed
	5	0 = Disabled, 1 = Enabled
	6	0 = Stream mode, 1 = Remote mode
	7	Reserved
2	0-7	Current resolution setting (D0 - D7)
3	0-7	Current sampling rate (D0 - D7)

n). Set Resolution (E8,XX)

EM84530 provides four resolutions selected by the second byte of this command as follows:

Second Byte XX	Resolution
00	8 dots/count
01	4 dots/count
02	2 dots/count
03	1 dots/count

o). Set Autospeed (E7)

At the end of a sample interval in the stream mode, the current X and Y data values are converted new values. The sign bits are not involved in this conversion. The conversion is only in stream mode. The relationship between the input and output count follows:

Input	Ouput
0	0
1	1
2	1
3	3
4	6
5	9
N($\geq$6)	2.0*N

p). Reset Autospeed (E6)

This command restore normal speed.

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(III) Microsoft PS/2 scrolling mouse mode:

   (A) Entering procedure: Except in WRAP mode, while EM84530 received the following consecutive command.

      i.   F3 C8   ---- set sampling rate 200/sec
      ii. F3 64   ---- set sampling rate 100/sec
      iii. F3 50   ---- set sampling rate 80/sec

   (B) Operating:

      a. All of the commands in legacy mode still be valid.

      b. The ID code of read device type command (F2) will changed from "00" to be "03".

      c. Data report will be four bytes format:

Byte	Bit	Description
1	0	Left button status; 1 = pressed
	1	Right button status; 1 = pressed
	2	Middle button status; 1 = pressed
	3	Reserve
	4	X data sign; 1 = negative
	5	Y data sign; 1 = negative
	6	X data overflow; 1 = overflow
	7	Y data overflow; 1 = overflow
2	0-7	X data (D0 - D7)
3	0-7	Y data (D0 - D7)
4	0-7	Reserved (0)

   (C) Exiting Microsoft PS/2 mouse mode:

      There are two ways to exit:

      a. Power off.
      b. Reset command (FF).

ABSOLUTE MAXIMUM RATINGS

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Parameter	Ratings		Unit
	Min.	Max.	
Temperature under bias	0	70	°C
Storage temperature range	-65	150	°C
Supply voltage	-0.3	7.5	V
Power dissipation	-	500	mW

DC ELECTRICAL CHARACTERISTICS (T_A=25°C to 70°C)

Parameters	Sym.	Min.	Typ.	Max.	Unit
X1,X2,Y1,Y2 low input reference current	I_{PL}	70	-	-	µA
X1,X2,Y1,Y2 high input reference current	IPh	-	-	106	µA
X1,X2,Y1,Y2 input current (80µA)	V_{PI1}	0.8	-	1.2	V
X1,X2,Y1,Y2 input current (500µA)	V_{PI2}	1.5	-	2.1	V
L,M,R,OPT input low voltage	Vail	-	-	1.5	V
L,M,R,OPT input high voltage	Vaih	3.5	-	-	V
L,M,R input current (Vin=5V)	Rmi	18	-	56	µA
OPT input current	Ixi	-18	-	-56	µA
OPT high output current (Iopt=20mA)	Vopt	4.5	-	-	V
L,M,R,X1,X2,Y1,Y2 input leakage current (Vin=0V)	Iil	0	-	-1.0	µA
OPT input leakage current (Vin=5V)	Iih	0	-	1.0	µA
OSC.OUT Sink current	Iosc	3	-	3.6	mA

PS/2 mouse mode : (T_A=25°C to 70°C)

Parameters	Sym.	Min.	Typ.	Max.	Unit
Operating voltage	VDD	4.5	-	5.5	V
Operating current (no load)	I_{Op}	-	-	1	mA
CLK, DATA postive-going threshold voltage	Vt+	3.2	-	3.8	V
CLK, DATA negative-going threshold voltage	Vt -	1.2	-	1.9	V
DATA, CLK input current (Vin=0V)	Idc	0.56	-	1.86	mA
DATA,CLK low output voltage (Iprl=-2mA)	Vprl	-	-	0.4	V

Serial mouse mode : (T_A=25°C to 70°C)

Parameters	Sym.	Min.	Typ.	Max.	Unit
Operating voltage	VDD	4	5.6	6.2	V
	VRTS	-	12.2	13	V
Operating current (VDD=5.4V, RTS=VDD)	IOP1	-	-	1	mA
Operating current (VDD=6.2V, RTS=VDD)	IOP2	3	-	-	mA
RTS operating current (VDD=5.6V, RTS=11V)	Irts1	-	-	1.8	mA
RTS operating current (VDD=5.4V, RTS=12.5V)	Irts2	4	-	-	mA
RXD high output voltage (Vrh=Vrts-2.6V)	Irh	2	-	-	mA
RXD low output voltage (Vrl=Vss+2.6V)	Irl	-	-	-2	mA
RTS high input voltage	Vrts+	9.6	-	-	V
RTS low input voltage	Vrts-	-	-	6.8	V

* All parameters in above table are compared with VSS

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AC ELECTRICAL CHARACTERISTICS (T$_A$=25°C to 70°C)

PS/2 mouse mode :

Parameters	Sym.	Min.	Typ.	Max.	Unit
Oscillating Frequency	Fosc	37.0-3%	37.0	37.0+3%	KHz
Key Debounce	Tkd	-	12	-	ms
Rising Edge Crossed Width	Tr	13.5	-	-	us
Falling Edge Crossed Width	Tf	13.5	-	-	us
Mouse CLK Active Time	Tmca	-	40.5	-	us
Mouse CLK Inactive Time	Tmci	-	40.5	-	us
Time that Mouse Sample DATA from CLK rising Edge	Tmdc	-	13.5	-	us
System CLK Active Time	Tsca	-	40.5	-	us
System CLK Inactive Time	Tsci	-	40.5	-	us
Time from DATA Transition to Falling Edge of CLK	Tsdc	-	13.5	-	us
Time from rising Edge of CLK to DATA Transition	Tscd	-	27.0	-	us
Time to mouse Inhibit after the 11th CLK to ensure mouse does not start another Transmission	Tpi	0	-	50	us

Serial mouse mode :

Parameters	Sym.	Min.	Typ.	Max.	Unit
Oscillating Frequency	Fosc	32.4-3%	32.4	32.4+3%	KHz
Key Debounce	Tkd	-	13	-	ms
Rising Edge Crossed Width	Tr	15.4	-	-	us
Falling Edge Crossed Width	Tf	15.4	-	-	us
Transmission Time	Tmx	-	33.33	-	ms
Start bit Time	Ts	-	833.3	-	us
Data bit time	Td	-	833.3	-	us
Stop bit Time	Tp	-	833.3	-	us
Mode code delay Time	Trd	11	-	14	ms

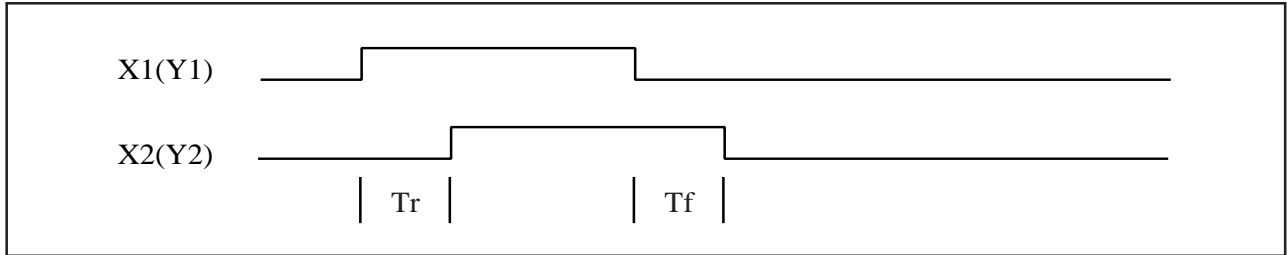X1, X2,Y1,Y2 Input Impedance

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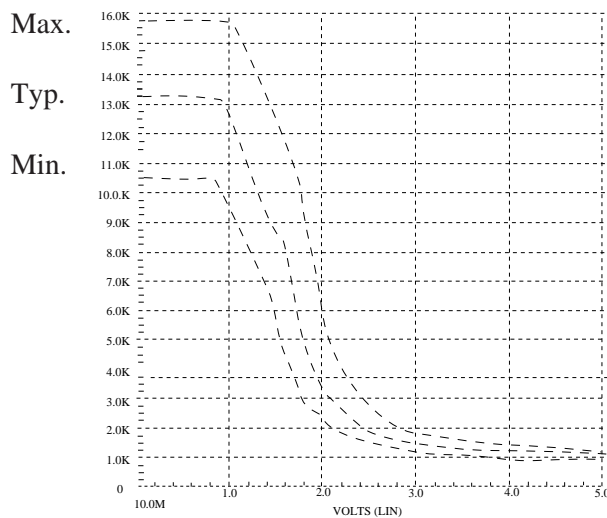
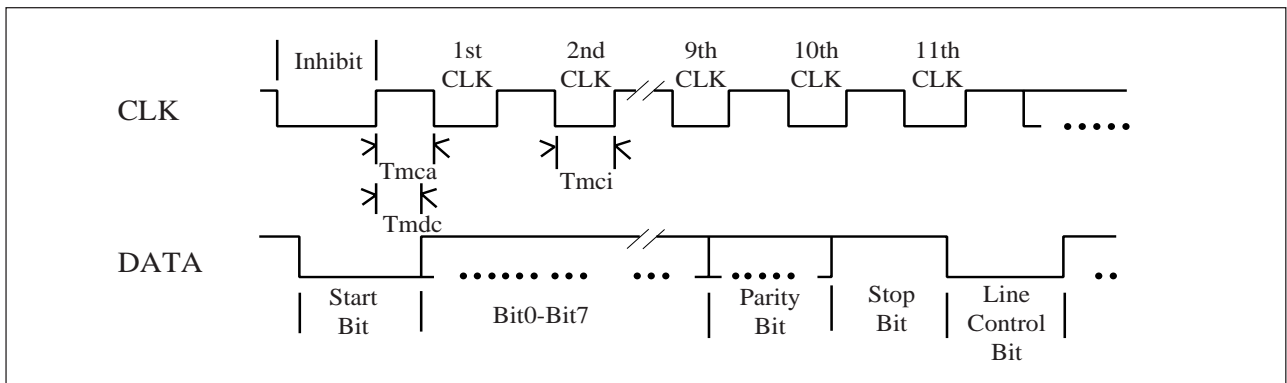** This specification are subject to be changed without notice.*

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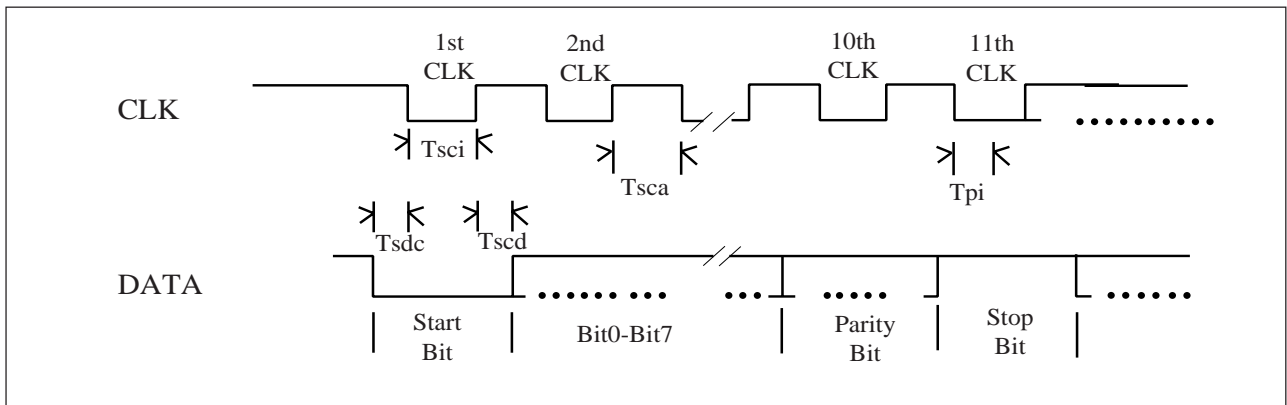## TIMING DIAGRAM

(A) PS/2 mouse mode

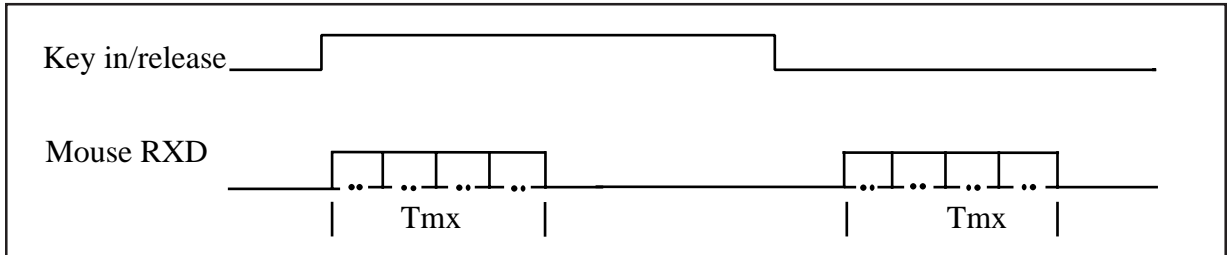#### (A) Photo-couples pulse width

(B) Receiving Data

(C) Sending Data

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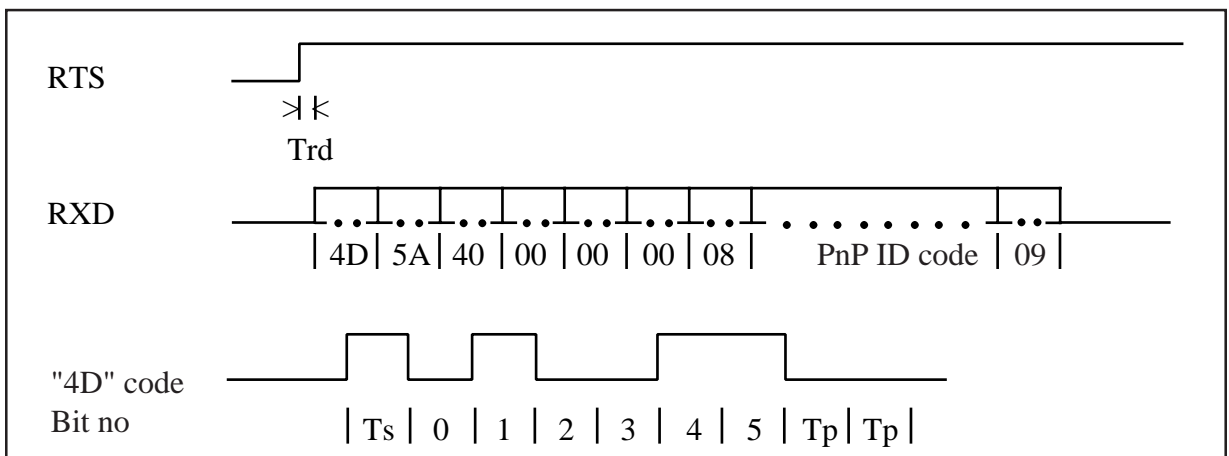**(B) Serial mouse mode :**

(A) RXD output :

Key in/release

Mouse RXD

| Tmx | | Tmx |

(B) Bytes structure :

RXD

••• 1st •• ••• 2rd •• ••• 3nd •• ••• 4nd ••

(C) Bits structure :

RXD LINE
Data Bit no.

| | 0 | 1 | 2 | 3 | 4 | 5 | 6 | | | | 0 | 1 | |
| Ts | Td | Td | Td | Td | Td | Td | Td | TP | Tp | | | |

(D) PnP ID code echo timing :

RTS

Trd

RXD

| 4D | 5A | 40 | 00 | 00 | 00 | 08 | PnP ID code | 09 |

"4D" code
Bit no

| Ts | 0 | 1 | 2 | 3 | 4 | 5 | Tp | Tp |
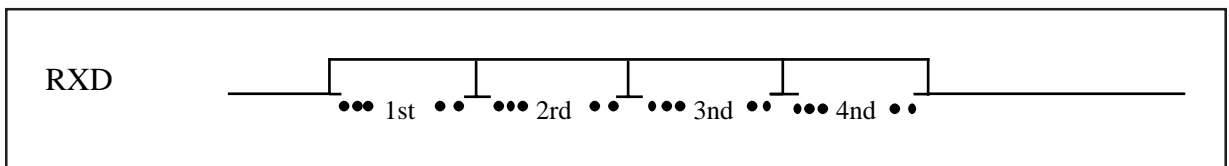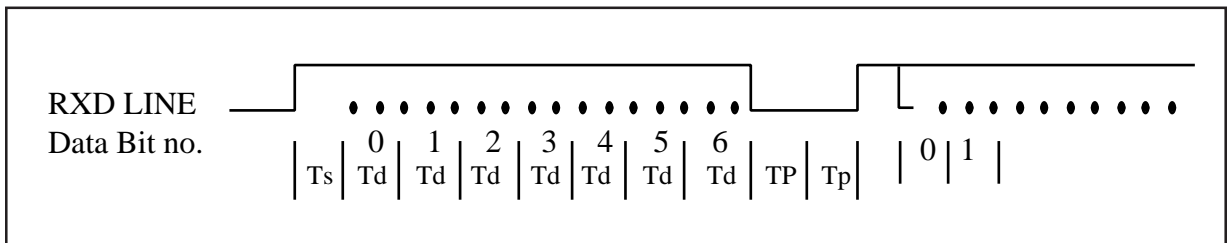
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APPLICATION CIRCUIT